



TPW

Docket No.: R2184.0247/P247  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Keiichi Yoshioka

Application No.: 10/633,681

Confirmation No.: 6878

Filed: August 5, 2003

Art Unit: 2825

For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE AND FABRICATION  
METHOD THEREOF

Examiner: S. Whitmore

**RESPONSE TO RESTRICTION REQUIREMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is in response to the restriction requirement set forth in the Office Action mailed July 6, 2005. Applicants hereby elect Group I, which includes claims 1-13, for continued examination without traverse. An action on the merits of all the claims and a Notice of Allowance thereof are respectfully requested.

Dated: August 8, 2005

Respectfully submitted,

By Mark J. Thronson Reg. No. 33,082  
Mark J. Thronson

Registration No.: 33,082  
DICKSTEIN SHAPIRO MORIN &  
OSHINSKY LLP  
2101 L Street NW  
Washington, DC 20037-1526  
(202) 785-9700  
Attorney for Applicant